Fig. 1 PRIOR ART

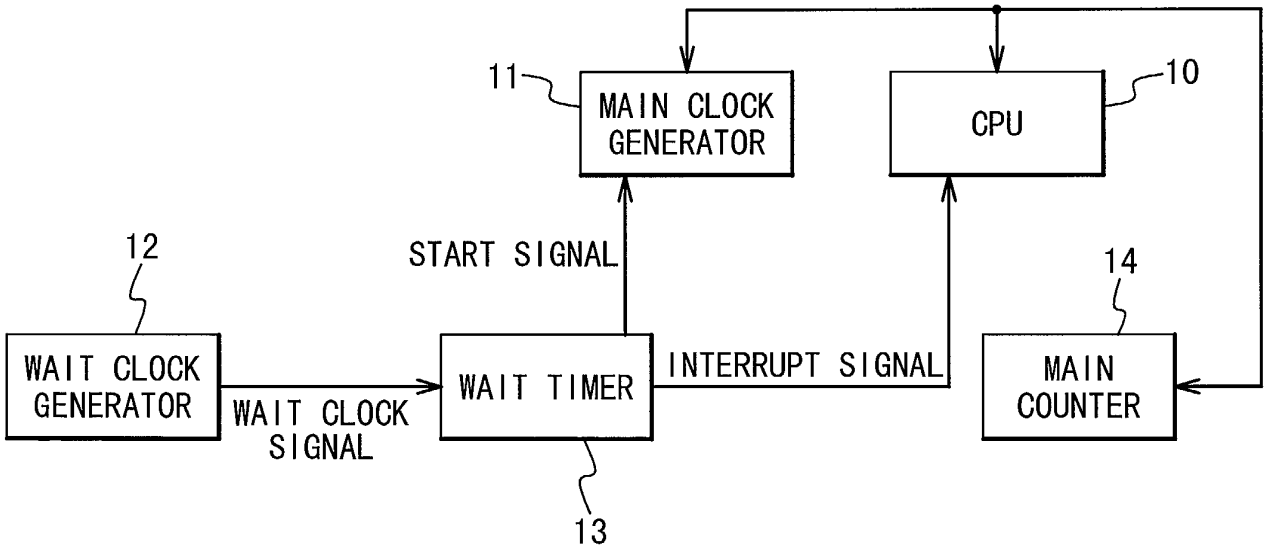


Fig. 2

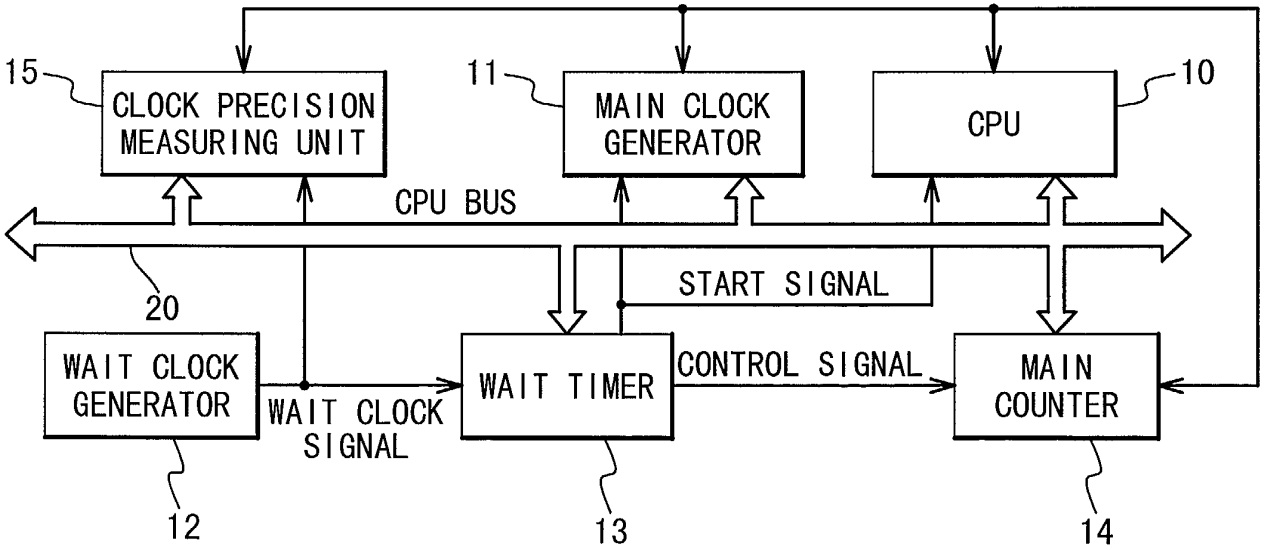

15
CLOCK PRECISION MEASURING UNIT
11
MAIN CLOCK GENERATOR
CPU
10
CPU BUS
20
START SIGNAL
WAIT CLOCK GENERATOR
WAIT CLOCK SIGNAL
WAIT TIMER
CONTROL SIGNAL
MAIN COUNTER
12
13
14

Fig. 3

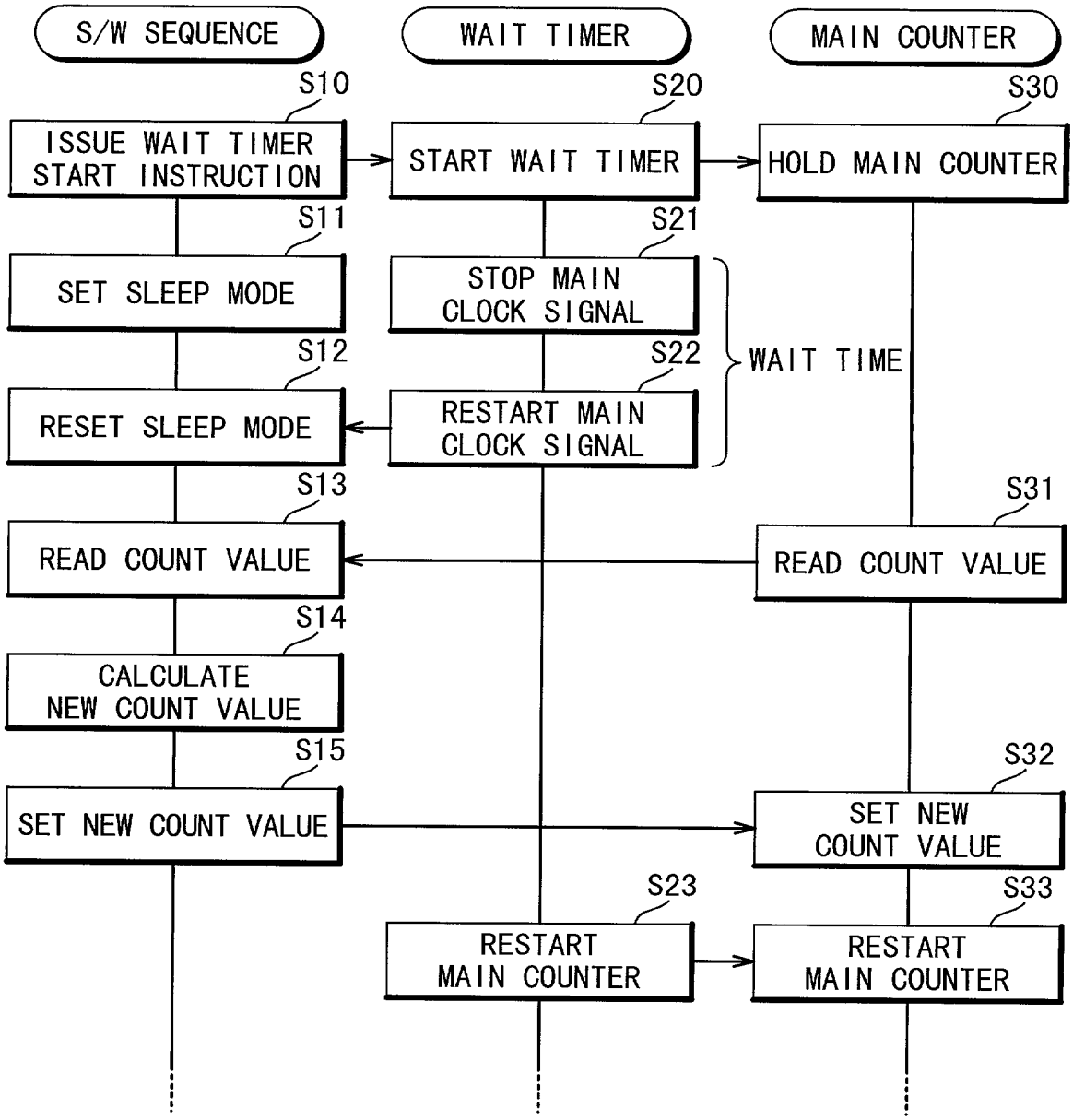

S/W SEQUENCE
WAIT TIMER
MAIN COUNTER
S10
ISSUE WAIT TIMER START INSTRUCTION
S20
START WAIT TIMER
S30
HOLD MAIN COUNTER
S11
SET SLEEP MODE
S21
STOP MAIN CLOCK SIGNAL
WAIT TIME
S12
RESET SLEEP MODE
S22
RESTART MAIN CLOCK SIGNAL
S13
READ COUNT VALUE
S31
READ COUNT VALUE
S14
CALCULATE NEW COUNT VALUE
S15
SET NEW COUNT VALUE
S32
SET NEW COUNT VALUE
S23
RESTART MAIN COUNTER
S33
RESTART MAIN COUNTER

Fig. 4

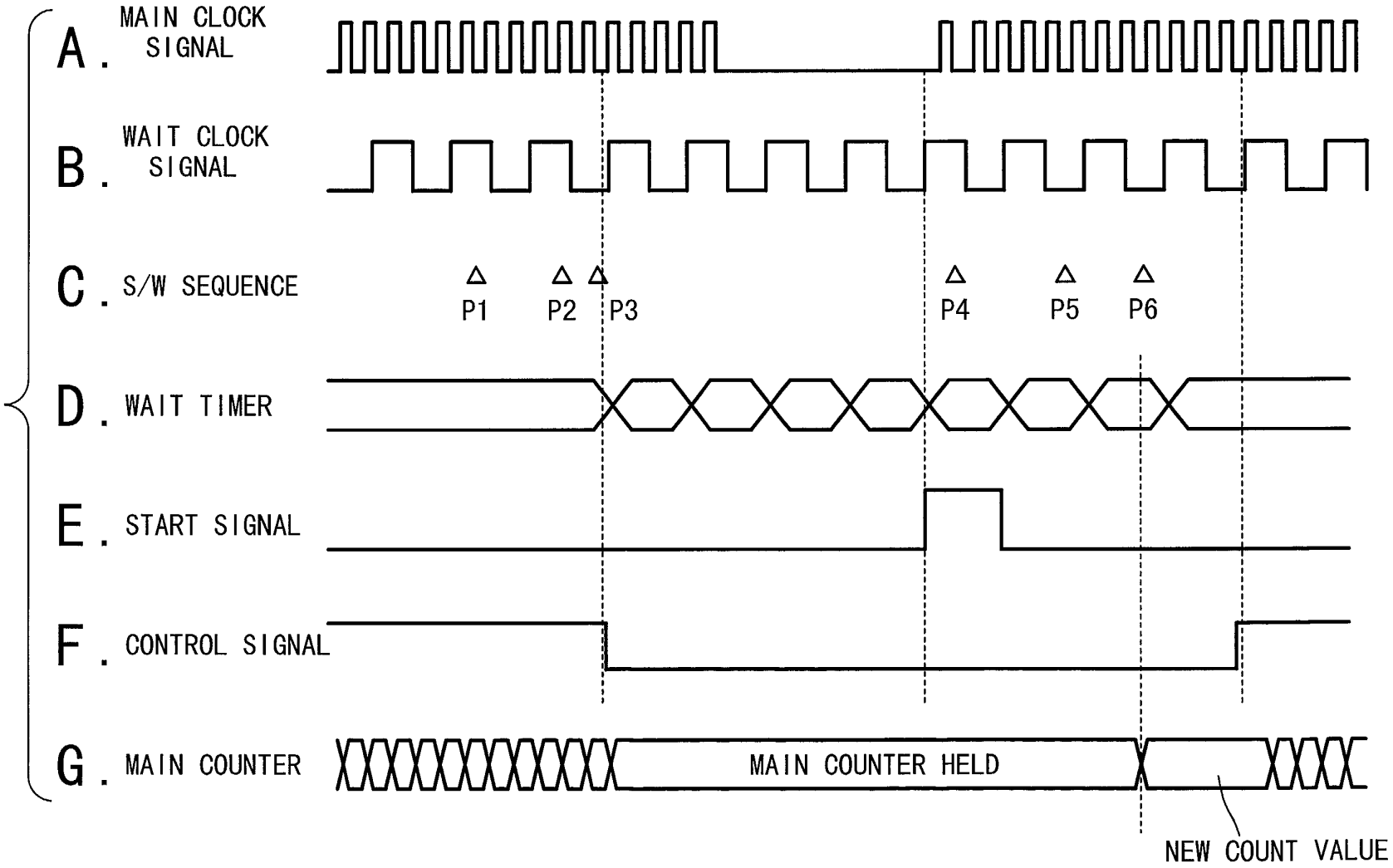
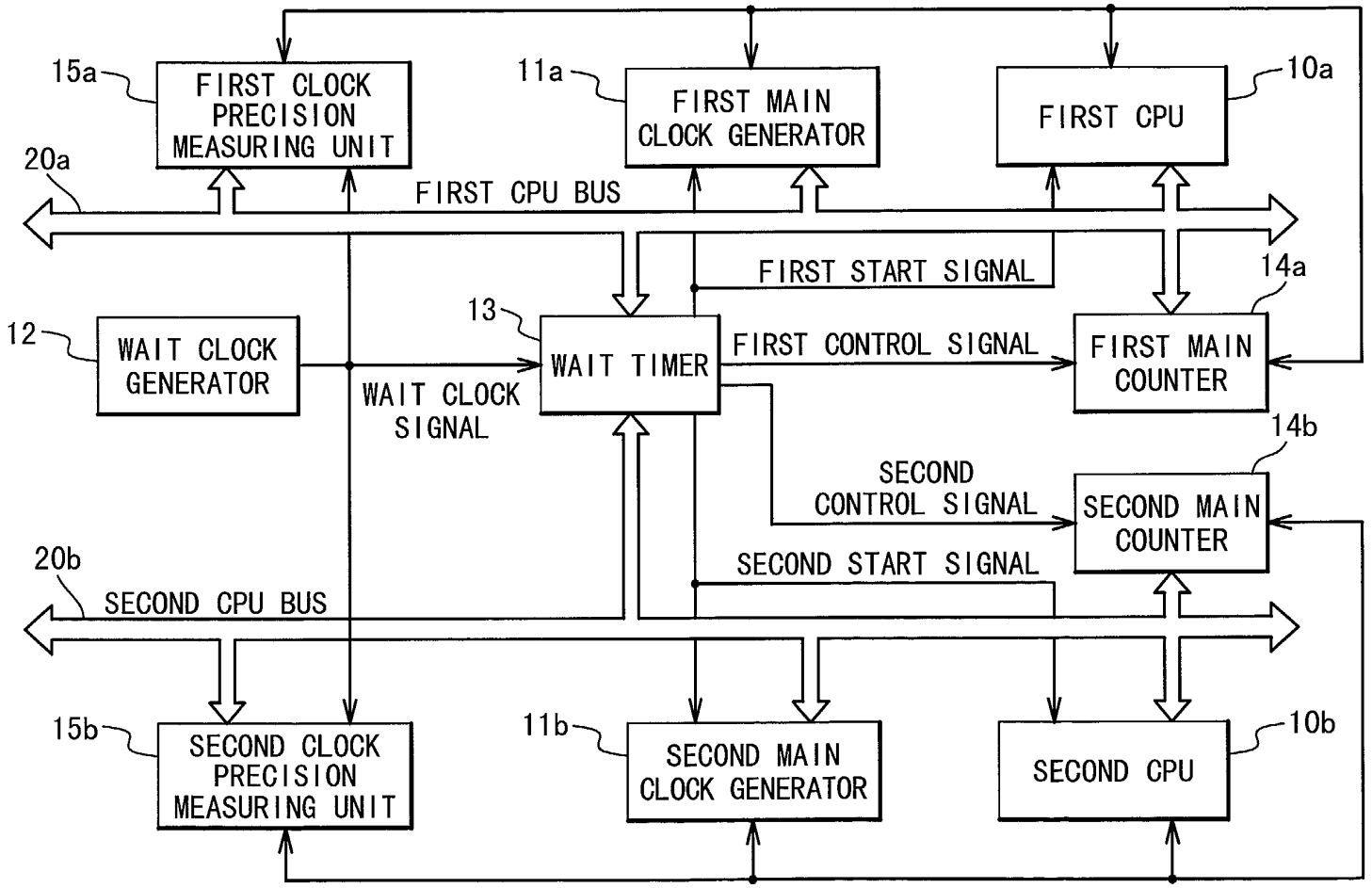

A. MAIN CLOCK SIGNAL
B. WAIT CLOCK SIGNAL
C. S/W SEQUENCE
P1
P2
P3
P4
P5
P6
D. WAIT TIMER
E. START SIGNAL
F. CONTROL SIGNAL
G. MAIN COUNTER
MAIN COUNTER HELD
NEW COUNT VALUE

Fig. 5


15a
FIRST CLOCK PRECISION MEASURING UNIT
11a
FIRST MAIN CLOCK GENERATOR
FIRST CPU
10a
20a
FIRST CPU BUS
FIRST START SIGNAL
14a
12
WAIT CLOCK GENERATOR
13
WAIT TIMER
FIRST CONTROL SIGNAL
FIRST MAIN COUNTER
WAIT CLOCK SIGNAL
14b
SECOND CONTROL SIGNAL
SECOND MAIN COUNTER
20b
SECOND START SIGNAL
SECOND CPU BUS
15b
SECOND CLOCK PRECISION MEASURING UNIT
11b
SECOND MAIN CLOCK GENERATOR
SECOND CPU
10b